CMP–28/32 Channel Modeling EDA Package

Package Contents:
- CMP-28 - 32 Gb/sec, CMP-32 - 50 Gb/sec
- S-parameters, pre-tested for quality
- PCB layout design files
- One library of your choice
- Keysight Technologies ADS
- Simbeor
- Mentor Hyperlynx-Nimbic
- Ansoft HFSS

The CMP-28 and CMP-32 Channel Modeling Platforms represent a powerful tool for development of high-speed 50 GHz systems. Optional low skew <1 psec matched cable kit. The primary target application for this product is 3D-EM solver analysis versus time and frequency domain measurement methodology. All structures include de-embedding to isolate the device-under-test (DUT) using Symmetrical De-embedding such as Automatic Fixture Removal (AFR, both 1X and 2X) or Measure-Modeled based.

EDA includes modeling of full path (includes VLF connector models) and de-embedded versions.