



A Fleet of Bit Error Rate Testers

- Comprehensive BERT offerings, from 125Gbps/lane to industry-leading 224Gbps/lane solutions
- Confident, accelerated development and deployment
- Applications Include:
 - Long Reach
 - Automotive cable testing
 - Stress testing
 - Jitter injection
 - PCIe gen 3, 5, 6, and 7
 - Ultra-high-density with rapid test times



Pulsar TDR for High-Throughput Cable Testing

- The most comprehensive solution for cabled host testing on the market
- Ultra-high resolution rise time
- Unparalleled insight into the performance and skew of cabled hosts
- High-value, high-throughput testing
- 100% coverage with minimal testing complexity



ML9004E Arbitrary Waveform Generator

- 4-Channel Differential Arbitrary Wave Form Generator
 - Selectable Baud Rate from 1 to 64 GBd with 8-bit DAC and user defined modulation
- Wide baud rate coverage allows PHY testing of Ethernet, PCIe Gen 4/5/6, USB and other standards
- Library of pre-defined waveforms
- Very fine bit rate tuning facilitates finding the locking margin
- Generate coherent signals for QAM modulation (horizontal and vertical I/Q pairs)
- Generate custom modulation in AWG mode include PAM6 and PAM8
- Independent control of inner eye levels
- Can also act as a two Dual-Channel (I/Q) Differential Pulse Pattern Generators (PPG)
 - Selectable Baud Rate from 25-64 GBd, NRZ/PAM-4 Modulation, and Independent 7-tap FFE on each transmitter



ML4081 Additive White Gaussian Noise Generator

- Wide band noise generator
- IEEE Interference
- 4 differential or 8 single ended channels
- Programmable Bandwidth 1-30 GHz
- Amplitude -30 dBm to 0 dBm
- Programmable Spectral Shaping
- Calibrated Amplitude accuracy 2 %
- Amplitude noise resolution 0.3 dB
- 2.4 dB Noise flatness to 30 GHz



Foundational 110GHz SMPX Connectors

- Pioneered by MultiLane
- Solderless mating design eliminates user error
- Reliable, repeatable performance across thousands of mating cycles
- Extremely scalable, with 1x16, 1x8, 1x4, and 1x2 orientations
- Designed for the density and SI required at 224Gbps/lane

